Interval Temporal Logic, Satisfiability and Model Checking

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Temporal Logic, Satisfiability and Model Checking

Interval Temporal Logic, Satisfiability and Model Checking

Part II: model checking

- introduction to interval temporal logic model checking
- the general picture
- the case of the logic AABBE
- what's next?



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Model checking

Model checking: the desired properties are checked against a model of the system

- the model is a (finite) state-transition system
- system properties are specified by a temporal logic (LTL, CTL, and the like)

Distinctive features of model checking:

- exaustive check of all the possible behaviours
- fully automatic process
- a counterexample is produced for a violated property

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Point-based vs. interval-based model checking

Model checking is usually point-based:

- properties express requirements over points (snapshots) of a computation (states of the state-transition system)
- they are specified by means of point-based temporal logics such as LTL and CTL

Interval-based properties express conditions on computation stretches, e.g., accomplishments, actions with duration, and temporal aggregations

Little work has been done on interval temporal logic (ITL) model checking (Bozzelli, Lomuscio, Michaliszyn, Molinari, Montanari, Murano, Perelli, Peron, Sala)

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Kripke structures



- HS formulas are interpreted over (finite) state-transition systems whose states are labeled with sets of proposition letters (Kripke structures)
- An interval is a trace (finite path) in a Kripke structure

A finite Kripke structure Ksched

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A short account of Ksched

 \mathcal{K}_{Sched} models the behaviour of a scheduler serving 3 processes which are continuously requesting the use of a common resource Initial state: v_0 (no process is served in that state) In v_i and \overline{v}_i the *i*-th process is served (p_i holds in those states) The scheduler cannot serve the same process twice in two

successive rounds:

- ▶ process *i* is served in state v_i , then, after "some time", a transition u_i from v_i to \overline{v}_i is taken; subsequently, process *i* cannot be served again immediately, as v_i is not directly reachable from \overline{v}_i
- ▶ a transition r_j , with $j \neq i$, from \overline{v}_i to v_j is then taken and process *j* is served

It can be easily generalised to an arbitrary number of processes

Some meaningful properties to be checked over Ksched

Validity of properties over all legal computation intervals can be forced by modality [E] (they are suffixes of at least one initial trace)

Property 1: in any computation interval of length at least 4, at least 2 processes are witnessed (YES/no process can be executed twice in a row)

$$\mathscr{K}_{Sched} \models [E](\langle \mathsf{E} \rangle^3 \top \to (\chi(p_1, p_2) \lor \chi(p_1, p_3) \lor \chi(p_2, p_3))),$$

where $\chi(p,q) = \langle \mathsf{E} \rangle \langle \overline{\mathsf{A}} \rangle p \land \langle \mathsf{E} \rangle \langle \overline{\mathsf{A}} \rangle q$

Property 2: in any computation interval of length at least 11, process 3 is executed at least once (NO/the scheduler can postpone the execution of a process ad libitum)

$$\mathcal{K}_{Sched} \not\models [E](\langle \mathsf{E} \rangle^{10} \top \to \langle \mathsf{E} \rangle \langle \overline{\mathsf{A}} \rangle p_3)$$

Property 3: in any computation interval of length at least 6, all processes are witnessed (NO/the scheduler should be forced to execute them in a strictly periodic manner, which is not the case)

$$\mathscr{K}_{\mathcal{S}ched} \not\models [E](\langle \mathsf{E} \rangle^5 \to (\langle \mathsf{E} \rangle \langle \overline{\mathsf{A}} \rangle p_1 \land \langle \mathsf{E} \rangle \langle \overline{\mathsf{A}} \rangle p_2 \land \langle \mathsf{E} \rangle \langle \overline{\mathsf{A}} \rangle p_3))$$

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HS semantics and model checking

Truth of a formula ψ over a trace ρ of a Kripke structure $\mathcal{K} = (\mathcal{AP}, W, \delta, \mu, w_0)$ defined by induction on the complexity of ψ :

- K, ρ ⊨ p iff p ∈ ∩_{w∈states(ρ)} µ(w), for any letter p ∈ AP (homogeneity assumption);
- negation, disjunction, and conjunction are standard;
- $\mathcal{K}, \rho \models \langle \mathsf{A} \rangle \psi$ iff there is a trace ρ' s.t. $\mathsf{lst}(\rho) = \mathsf{fst}(\rho')$ and $\mathcal{K}, \rho' \models \psi$;
- $\mathcal{K}, \rho \models \langle \mathsf{B} \rangle \psi$ iff there is a prefix ρ' of ρ s.t. $\mathcal{K}, \rho' \models \psi$;
- $\mathcal{K}, \rho \models \langle \mathsf{E} \rangle \psi$ iff there is a suffix ρ' of ρ s.t. $\mathcal{K}, \rho' \models \psi$;
- the semantic clauses for $\langle \overline{A} \rangle$, $\langle \overline{B} \rangle$, and $\langle \overline{E} \rangle$ are similar

Model Checking

 $\mathcal{K} \models \psi \iff$ for all *initial* traces ρ of \mathcal{K} , it holds that $\mathcal{K}, \rho \models \psi$

Possibly infinitely many traces!

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Remark: HS state semantics

 According to the given semantics, HS modalities allow one to branch both in the past and in the future



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The key notion: BE_k -descriptor

- The BE-nesting depth of an HS formula ψ (Nest_{BE}(ψ)) is the maximum degree of nesting of modalities B and E in ψ
- Two traces ρ and ρ' of a Kripke structure 𝔆 are k-equivalent if and only if 𝔅, ρ ⊨ ψ iff 𝔅, ρ' ⊨ ψ for all HS-formula ψ with Nest_{BE}(ψ) ≤ k

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We provide a suitable tree representation for a trace, called a BE_k -descriptor

The *BE*_{*k*}-descriptor for a trace $\rho = v_0v_1..v_{m-1}v_m$, denoted *BE*_{*k*}(ρ), is defined as follows:



Remark: the descriptor has not sibling isomorphic subtrees

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An example of a *BE*₂-descriptor



 $(v_0, \{v_0, v_1\}, v_0) \quad (v_0, \{v_1\}, v_0) \quad (v_0, \{\}, v_1) \quad (v_0, \{v_1\}, v_0) \quad (v_0, \{\}, v_1) \quad (v_0, \{\}, v_1)$

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An example of a *BE*₂-descriptor



Remark: the subtree to the left is associated with both prefixes $v_0v_1v_0^3$ and $v_0v_1v_0^4$ (the descriptor has not sibling isomorphic subtrees)

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FACT 1: For any Kripke structure \mathcal{K} and any BE-nesting depth $k \ge 0$, the number of different BE_k -descriptors is finite (and thus at least one descriptor has to be associated with infinitely many traces)

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Theorem

The model checking problem for full HS on finite Kripke structures is decidable (with a non-elementary algorithm)



A. Molinari, A. Montanari, A. Murano, G. Perelli, and A. Peron, Checking Interval Properties of Computations, Acta Informatica (to appear)

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What about lower bounds?

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The logic BE

Theorem

The model checking problem for BE, over finite Kripke structures, is EXPSPACE-hard

L. Bozzelli, A. Molinari, A. Montanari, A. Peron, and P. Sala, Interval Temporal Logic Model Checking: The Border Between Good and Bad HS Fragments, IJCAR 2016

Proof (sketch): a polynomial-time reduction from a domino-tiling problem for grids with rows of single exponential length

- ▶ for an instance I of the problem, we build a Kripke structure K_I and a formula φ_I in polynomial time
- there is an initial trace of \mathcal{K}_I satisfying φ_I iff there is a tiling of I
- $\mathcal{K}_{I} \models \neg \varphi_{I}$ iff there exists no tiling of I

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BE hardness: encoding of the domino-tiling problem

Instance of the tiling problem: $(C, \Delta, n, d_{init}, d_{final})$, with *C* a finite set of colors and $\Delta \subseteq C \times C \times C \times C$ a set of tuples (c_B, c_L, c_T, c_R)



String (interval) encoding of the problem



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The general picture



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Three main gaps to fill

The picture shows that there three main gaps to fill:

- full HS and BE are in between nonELEMENTARY and EXPSPACE
- ► AABBE, AAEBE, ABBE, AEBE, ABBE, and AEBE are in between EXPSPACE and PSPACE
- ► A, A, AA, AB, and AE are in between P^{NP[O(log² n)]} and P^{NP[O(log n)]}

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Let us consider the case of the logic $A\overline{A}B\overline{BE}$, which is obtained from full HS ($A\overline{A}B\overline{EBE}$) by removing modality $\langle E \rangle$

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The logic AABBE

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Some fundamental facts:

• we can restrict our attention on prefixes (B_k -descriptors suffice)

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- a trace representative can be chosen to represent a (possibly infinite) set of traces with the same B_k-descriptor

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- we can restrict our attention on prefixes (B_k -descriptors suffice)
- the size of the tree representation of B_k-descriptors is larger than necessary (redundancy) and it prevents their efficient exploitation in model checking algorithms
- ► a trace representative can be chosen to represent a (possibly infinite) set of traces with the same B_k-descriptor
- a bound, which depends on both the number |W| of states of the Kripke structure and the B-nesting depth k, can be given to the length of trace representatives

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Traces and sequences of descriptor elements



Let us consider the trace $\rho = v_0 v_0 v_0 v_1 v_2 v_1 v_2 v_3 v_3 v_2 v_2$

The descriptor element $DElem(\rho)$ for ρ :

 $(v_0,\{v_0,v_1,v_2,v_3\},v_2)$

The descriptor sequence ρ_{ds} for ρ (Δ_i stands for { v_0, \ldots, v_i }):

 $(v_0, \emptyset, v_0)(v_0, \Delta_0, v_0)(v_0, \Delta_0, v_1)(v_0, \Delta_1, v_2)(v_0, \Delta_2, v_1)(v_0, \Delta_2, v_2)$ $(v_0, \Delta_2, v_3)(v_0, \Delta_3, v_3)(v_0, \Delta_3, v_2)(v_0, \Delta_3, v_2)$

The descriptor sequence is the sequence of the descriptor elements for ρ and for its prefixes in increasing order (from the one for the shortest prefix to the one for the whole trace)

A contraction method

- Repeated occurrences of the same descriptor element in a descriptor sequence ρ_{ds} represent prefixes of a trace which unwind a loop in a Krypke structure
- Two occurrences of the same descriptor element in a descriptor sequence \(\rho_{ds}\) are k-indistinguishable if the associated trace prefixes have the same B_k-descriptor
- If two repeated occurrences are k-indistiguinshable, we can contract the trace avoiding the second repetition

$$B_{k}(\rho(0,i)) = B_{k}(\rho(0,j))$$

$$\rho(i) \rho(j)$$

$$\rho(j) \rho(j) \rho(j)$$

$$\rho(j) \rho(j)$$

$$\rho(j$$

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Boundedness theorem

given a trace ρ, we can repeatedly contract it until it has not occurrences of k-indistiguishable descriptor elements

Theorem (Boundedness theorem)

If ρ is a trace of a Kripke structure \mathcal{K} , with set of states W, and $k \ge 0$, then there exists a trace ρ' , with the same B_k -descriptor as ρ , such that

$$|\rho'| \le \tau(|W|, k) = \min \left\{ \begin{array}{l} 1 + (1 + |W|)^{2k+4} + |W| \\ 1 + (k+3)^{|W|^2+1} + |W| \end{array} \right\}$$

- If |ρ| > τ(|W|, k), then ρ necessarily has some occurrences of k-indistiguishable descriptor elements
- termination criterion: when enumerating traces, it is enough to consider traces of length less then or equal to τ(|W|, k)

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The model checking algorithm

 $\mathsf{ModCheck}(\mathcal{K},\psi)$

- 0: $k \leftarrow \text{Nest}_{\mathsf{B}}(\psi)$
- 0: $u \leftarrow New(\text{Unravel}_from(\mathcal{K}, \text{init}_state(\mathcal{K}), k, \text{FORWARD}))$
- 0: while u.hasMoreTraces() do
- 0: $\overline{\rho} \leftarrow u.getNextTrace()$
- 0: if $Check(\mathcal{K}, k, \psi, \overline{\rho}) = 0$ then return 0: " $\mathcal{K}, \overline{\rho} \not\models \psi$ " return 1: " $\mathcal{K} \models \psi$ " =0

EXPSPACE: $(|\psi|+1) \cdot O(|W| + \text{Nest}_B(\psi)) \cdot \tau(|W|, \text{Nest}_B(\psi))$ bits

A. Molinari, A. Montanari, and A. Peron, A Model Checking Procedure for Interval Temporal Logics based on Track Representatives, CSL 2015

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PSPACE-hardness of AABBE model checking

PSPACE-hardness of the model checking problem for the fragment \overline{B} (and thus for \overline{AABBE}) can be proved by a reduction from the QBF problem

Theorem

The model checking problem for \overline{B} , and thus for \overline{ABBE} , over finite Kripke structures is PSPACE-hard.

Remark: *AABBE* model checking is in between PSPACE and EXPSPACE (remind: *BE* is EXPSPACE-hard)



A. Molinari, A. Montanari, A. Peron, and P. Sala, Model Checking Well-Behaved Fragments of HS: The (Almost) Final Picture, KR 2016

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Current research agenda

- To complete the picture of interval temporal logic model checking under the homogeneity assumption (and the HS state semantics)
- To explore alternative HS semantics. In particular, the trace semantics, where the infinite paths (computations) of the Kripke structure are the main semantic entities, and the computation tree semantics, where future is branching, but past is linear (as well as finite and cumulative). Trace (resp., computation tree) semantics allows us to establish a bridge between HS (model checking) and LTL (resp., CTL) (model checking)
- To remove the homogeneity assumption

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Epistemic HS (Lomuscio and Michaliszyn)

Distinctive feature of Epistemic HS (EHS for short): the labelling function is defined on the endpoints of the (finite) traces/intervals

Lomuscio and Michaliszyn proved that the local model checking problem (verification of a given specification against a single initial trace) for the fragment *EHS*[*BE*] is **PSPACE**-complete

If epistemic modalities are removed, it is in PTIME (notice that modalities *B* and *E* allow one to access only sub-intervals of the given initial one, whose number is quadratic in the length of it)

A. Lomuscio and J. Michaliszyn, An Epistemic Halpern-Shoham Logic, IJCAI 2013

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Epistemic HS (Lomuscio and Michaliszyn) - cont'd

Later on, they showed that the picture drastically changes with other fragments of HS that allow one to access infinitely many traces

They proved that the model checking problem for the HS fragment $A\overline{B}$, extended with epistemic modalities, is decidable, with a non-elementary upper bound

Notice that formulas of this logic can possibly refer to infinitely many (future) traces



A. Lomuscio and J. Michaliszyn, Decidability of model checking multi-agent systems against a class of EHS specifications, ECAI 2014

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Epistemic HS (Lomuscio and Michaliszyn) - cont'd

In their most recent contribution, Lomuscio and Michaliszyn generalized the labeling function by allowing it to be given by any regular expression on the states of intervals

Such a generalization results in a considerable increase in the expressiveness of the specifications at no computational cost in terms of the corresponding model checking problem

A. Lomuscio and J. Michaliszyn, Model Checking Multi-Agent Systems against Epistemic HS Specifications with Regular Expressions, KR 2016

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Mid- and long-term research agenda

- Systematic application of game-theoretic techniques in interval-based synthesis
- Quest for automaton-based techniques for interval temporal logic satisfiability and model checking
- Application of interval temporal logics to

(i) system specification, verification, and synthesis

(ii) planning and plan validation (to represent and to reason about actions/events with duration, accomplishments, and interval constraints)
(iii) temporal databases (to deal with temporal aggregation) and workflow systems (to cope with additional temporal constraints)

 Application of interval temporal logic model checking to infinite state systems

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